

Known Bugs in ETEC Version 2.21

Bug Identifier	Source	Problem/Bug Description	Severity	Workaround Description	Affected Releases	Fixed Release
V1.00D-5 (2009-Dec-15)	internal	When the sizeof operator is applied to a constant the wrong size may result, e.g. sizeof(1) may result in "1" rather than the expected "3" bytes.	2	Take the sizeof of the desired type instead: sizeof(int)	All versions	TBD
V1.20A-14 (2009-May-20)	internal	Chan interrupt opcodes may be moved relative to adjacent RAM instructions by the optimizer. This may cause unexpected results, particularly in the case of a DMA interrupt.	3	Use <code>_OptimizationBoundaryAll()</code> or <code>#pragma optimization_boundary_all</code> if there is concern that an interrupt may cross a critical RAM access.	All versions	TBD
V1.25A-11 (2009-Sep-28)	internal	If pointer arithmetic generates a negative result, and the object pointed to is larger than 1 byte in size, ETEC code will generate an incorrect result. This is because an unsigned shift (or unsigned divide) is applied after the pointer arithmetic to convert from byte addressing to object indexing.	3	Keep pointer arithmetic results in the non-negative domain.	All versions	TBD
V1.25B-6 (2009-Dec-9)	internal	The <code>_STACK_SIZE_</code> defines macro gets the calculated value of the worst-case stack depth. In certain rare cases, this value can be slightly larger than the actual worst-case. This can occur when a stack usage of a register save and restore (e.g. in a called C function) is eliminated via optimization. Such a register save requires 4 bytes of stack space, but the removal of it is not currently getting accounted for in the stack size calculation.	4	Care should be taken in that in some rare cases, a <code>_STACK_SIZE_</code> value that is non-zero can still mean that no stack is actually utilized. Another way to verify that no stack is used is to make sure that no <code><func/class name>_STACKBASE_</code> macros are defined.	All versions	TBD

V1.25B-7 (2009-Dec-11)	internal & customer	The optimizer/analyzer does not yet support reentrant functions, whether they be callable C functions or ETEC code fragments. Reentrance is supposed to be detected and cause an error, but in some cases this detection failed, allowing for optimization to continue. Sometimes the result could be a linker crash, or sometimes invalid code generation, or in some cases working code resulted.	3	Avoid writing reentrant functions until the ETEC optimizer/analyzer fully supports them.	All versions	V1.25C (reentrance detection), TBD (support reentrance)
V2.21A-1 (2013-Oct-15)	customer	Several temporary files are generated during the compilation of a translation unit. Currently these are being generated in the source directory, but they should be generated in the output directory instead (most of the time the source and output directory are the same). The current behavior causes problems when the source directory is read-only.	4	Copy the source files to a writeable directory before compiling.	All versions	V2.22A
V2.21A-2 (2013-Feb-28)	customer	The compiler optimizer has a 'Register Tracking' bug in which the optimizer fails to invalidate the ertA and ertB register on a chan register write even though a chan register write causes both registers to be overwritten with the values in the captureA and captureB registers of the new channel. This can result in an incorrect ertA or ertB value being used in an expression after a channel change.	2	Placing an optimization boundary just before or after the problem channel change stops the failure from occurring. For example: #pragma optimization_boundary_all; SwitchToChannel(newChan);	V2.00A - V2.21A	V2.22A

Bug Severity Level Descriptions:

1 – Problem causes complete work stoppage. No work-around is possible. The problem is likely to be hit by most users. This level of bug will typically trigger a new release or patch in a short time frame.

2 – A difficult problem to track down, such as incorrectly generated code. Typically there is a work-around available for this kind of bug.

3 – A bug that is easy to spot, and/or generally has a straight-forward work-around, or has minimal impact.

4 – Not truly a bug (i.e. tool is within spec.), but rather something that might affect compatibility or usability. Work-arounds available.